**Student Name:**

**UID:**

**Date of Performance:**

**Branch: Section/Group:**

**Subject Name**

**Aim**

Validate truth table for:

* NAND gates HD74LS00
* NOR gates HD74LS02
* NOT gates HD74LS04
* AND gates HD74LS08
* XOR gates HD74LS86

**Task to be done**

*(Objective of the task to be explained)*

**Requirements**

*(Hardware and software requirements)*

**Circuit diagram/ Block diagram**

*(Insert circuit diagram here)*

**Simulation Results:**

*(Insert simulation results for given conditions)*

**Both inputs🡪HIGH, OUTPUT🡪?**

**One input🡪LOW & other input🡪HIGH, OUTPUT🡪?**

**One input🡪HIGH & other input🡪LOW, OUTPUT🡪?**

**Both inputs🡪LOW, OUTPUT🡪?**

**Concept used**

*(Point out the concepts used in order to design the solution)*

**Learning/ observation**

*(Observations made during the experiment and learnings for future reference)*

**Troubleshooting**

*(Problems encountered and how did you solved those)*